

IN STOCK

3-Phase Voltage Monitors

✓ *Motor Protection Not Fooled by Regenerated Voltages*

11

CONTROLS & SENSORS

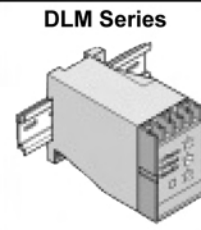
Solid State Protection You Can Rely On



4.50" x 3.13" x 1.35"
(114x80x34mm)



1.78" x 2.39" x 3.03"
(45x61x77mm)



4.33" x 2.95" x 1.97"
(110x75x50mm)

Protection	Phase Loss	●	●	●
	Phase Reversal	●	●	●
	Voltage Unbalance	●	●	●
	Low Voltage	●	●	●
	High Voltage	●	●	●
Adjustable Voltage Ranges	200 to 240VAC	●	●	●
	355 to 425VAC	●	●	●
	400 to 480VAC	●	●	●
	500 to 600VAC	●	●	●
LED Indicators	Normal Operation (ON)	●	●	●
	Trip Delay	2 to 20 Sec. Adj.	0.25 to 30 Sec. Adj.	2 to 20 Adj.
	Voltage Unbalance	2-6% Adj.	2 to 10% Adj.	2 to 8% Adj.
	Output Contacts SPDT (Resistive)	8 Amps	10 Amps	10 Amps
	Agency Approvals			

Part Number	Line Voltage (Adjustable)	Voltage Unbalance	Trip Delay	Connection
PLMU11	200 to 480VAC*	Adj. 2 to 10%	Adj. 0.25 to 30 Seconds	Fig. 1

*Use Socket OT08 Rated for 480VAC Use

Part Number	Line Voltage (Adjustable)	Voltage Unbalance	Trip Delay	Connection
RLM611	240VAC	Adj. 2 to 6%	Adj. 2 to 20 Seconds	Fig. 2
RLM911	480VAC			

Other Voltages and Options Available

Part Number	Line Voltage (Adjustable)	Unbalance (Adjustable)	Trip Delay (Adjustable)	Connection
DLM611	200 to 240VAC	2% to 8%	2 to 20 Secs.	Fig. 3
DLM911	400 to 480VAC			
DLM011	500 to 600VAC			

Other Voltages and Options Available

RLM Series

- Encapsulated
- Low Cost OEM
- .25"(6.35mm) Quick Connects
- Automatic Reset
- Adj. Voltage, Unbalance, & Trip Delay

PLMU Series

- Standard 8 Pin Plug-in Package
- Universal Voltage
- Automatic Reset
- Adj. Voltage, Unbalance & Trip Delay

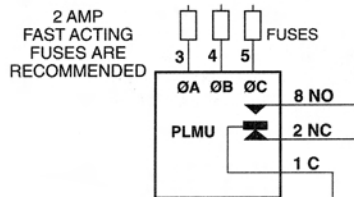
DLM Series

- 35mm DIN Rail or Surface Mounting
- 50mm Package with Touch Proof Terminals
- Encapsulated Circuitry
- Adj. Voltage, Unbalance, & Trip Delay
- Automatic Reset

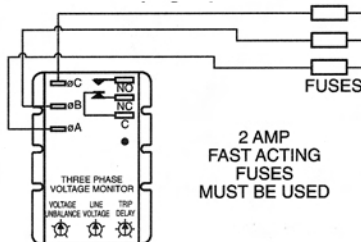
Key Features

- Prevents Motor Burnout
- Prevents Expensive Down Time
- Universal Design – One Monitor for any Size Motor
- 3 Wire Delta or Wye Connection
- New "Easy Set" Design Eliminates Nuisance Tripping
- LED "ON" Indicates Normal Operation

PLMU (Fig. 1)



RLM (Fig. 2)



DLM (Fig. 3)

