



FEATURES

1. High-speed switching

Since release time is 0.1 ms, the MOSFET can be turned off quickly in urgent situations.

2. Space saving With a built-in control circuit, an external resistor is not needed. This contributes to making substrates more compact.

3. High insulation DIP type: 5,000 V SOP type: 2,500 V SSOP type: 1,500 V

4. Extensive product lineup Products include the industry's smallest SSOP type, SOP4 type, and DIP 6 type.

APV1,2

TYPICAL APPLICATIONS

MOSFET driver Power supply (Vcc) for electronic circuits



30

20

Output rating Part No. Packing quantity Through hole Surface-mount terminal terminal Drop-out Shortcircuit Type voltage current Tape and reel packing style Tube packing Tube packing Tape and (Typ.) (Typ.) Tube Picked from Picked from style style reel 1/2/3-pin side*1 4/5/6-pin side*2 1 tube contains 50 pcs. APV1122AZ APV1122AX DIP6pin APV1122 APV1122A 8.7V 14µA 1 batch contains 500 pcs. SOP4pin 8.7V APV1121SX APV1121SZ 14µA _ ____ 1 tube contains 50 pcs. 1,000 pcs. 1 batch contains 500 pcs. SOP4pin*3 8.2V 8µA APV2121SX APV2121SZ ____ ____ SSOP*4 8.2V 8µA APV2111VY APV2111VW

Notes: *1 SOP type is picked from 1/2-pin side, SSOP type is picked from 1/4-pin side

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______ 0.3

*2 SOP type is picked from 3/4-pin side, SSOP type is picked from 2/3-pin side

*3 Tape package is the standard packing style. Also available in tube. (Part No. suffix "X" or "Y" is not needed when ordering; Tube: 100 pcs.; Case: 2,000 pcs.) For space reasons, the initial letters of the product number "AP" and "S" are omitted on the product seal.

The package type indicator "X" and "Z" are omitted from the seal. (Ex. the label for product number APV1121SX is V1121).

*4 Tape package is the standard packing style. For space reasons, the initial letters of the product number "AP" and "V" are omitted on the product seal. The package type indicator "Y" and "W" are omitted from the seal. (Ex. the label for product number APV2111VY is V2111).

RATING

1. Absolute maximum ratings (Ambient temperature: 25°C 77°F)

Item		Symbol	APV1122(A)	APV1121S	APV2121S	APV2111V	Remarks
Input	LED forward currer	t I⊧					
	LED reverse voltag	e Vr					
	Peak forward curre	nt I _{FP}		f = 100 Hz, Duty Ratio = 0.1%			
	Power dissipation	Pin	75mA				
I/O isolation voltage		Viso	5,000V AC	2,500V AC	2,500V AC	1,500V AC	
Tempera limits	ture Operating	Topr		Non-condensing at low temperatures			
mints	Storage	Tstg					

MOSFET Driver ASCT1B274E '03.7

New

APV1,2

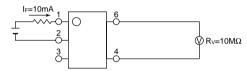
2. Electrical characteristics (Ambient temperature: 25°C 77°F)

Item				APV1122(A)	APV1121S	APV2121S	APV2111V	Condition
		Typical	1-	0.6mA		0.85mA		Voc = 5V
	LED operate current	Maximum	Fon		3n	nA		
Input	LED turn off current	Minimum	Foff	0.2mA				Voc = 1V
mput		Typical	IFOT	0.5mA		0.75mA		V0C = 1V
	LED dropout voltage	Typical	VF	1.15V				I⊧ = 10mA
		Maximum	VF	1.5V				
	Drop-out voltage*	Minimum	Voc	6V		5V		- I⊧ = 10mA
Output		Typical	VOC	8.7V		8.2V		
Output	Short circuit current**	Minimum	lsc	5μ	5μΑ		3µA	
		Typical	150	14	μA	8μΑ		- I⊧ = 10mA
	Turn on time***	Typical	Ton	0.4	0.4ms 0.8ms		ims	I _F = 10mA, C _L = 1,000pF
Transfer characteristics	Turn off time*** Typical		Toff	0.1ms			I _F = 10mA, C _L = 1,000pF	
	I/O consoitance	Typical	C	0.8pF			$V_B = 0V,$ f = 1MHz	
	I/O capacitance	Maximum	Ciso	1.5pF				
	Initial I/O isolation resistance	Minimum	Riso	1,000ΜΩ				500V DC

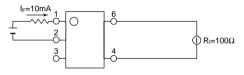
Note: Recommended LED forward current IFT: 10mA.

*Drop-out voltage measurement circuit

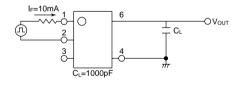
APV1122(A)



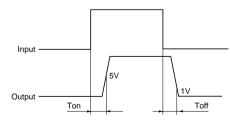
**Short circuit current measurement circuit APV1122(A)



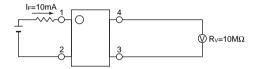
***Turn on/Turn off time measurement circuit APV1122(A)



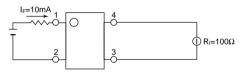
***Turn on time



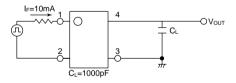
APV1121S, APV2121S, APV2111V



APV1121S, APV2121S, APV2111V



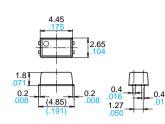
APV1121S, APV2121S, APV2111V



mm inch

DIMENSIONS

1. APV2111V



Terminal thickness: 0.15 .006General tolerance: $\pm 0.1 \pm .004$

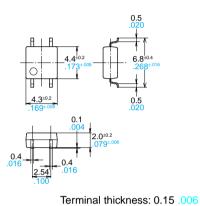
Recommended mounting pad (TOP VIEW)



Tolerance: $\pm 0.1 \pm .004$

Tolerance: $\pm 0.1 \pm .004$

2. APV1121S, APV2121S

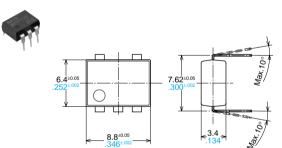


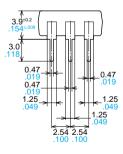
(TOP VIEW)

Recommended mounting pad

General tolerance: $\pm 0.1 \pm .004$

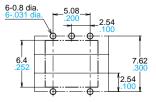
3. (1) APV1122



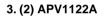


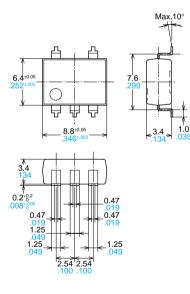
Terminal thickness: 0.25 .010 General tolerance: $\pm 0.1 \pm .004$

PC board pattern (BOTTOM VIEW)



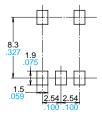
Tolerance: $\pm 0.1 \pm .004$





Terminal thickness: 0.25 .010 General tolerance: ±0.1 ±.004

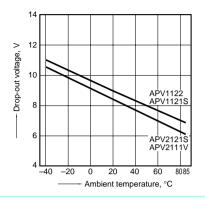
Recommended mounting pad (TOP VIEW)



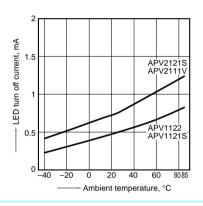
Tolerance: $\pm 0.1 \pm .004$

REFERENCE DATA

1. Drop-out voltage vs. ambient temperature characteristics Input current: 10mA

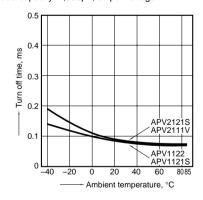


4. LED turn off current vs. ambient temperature characteristics Drop-out voltage: 1V

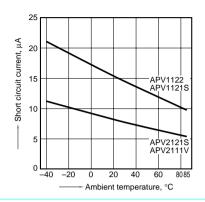


7. Turn off time vs. ambient temperature characteristics

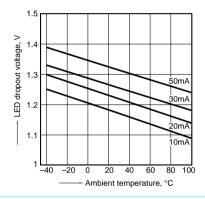
LED forward current: 10mA Load capacity: 1,000pF; output voltage: 1V



2. Short circuit current vs. ambient temperature characteristics Input current: 10mA

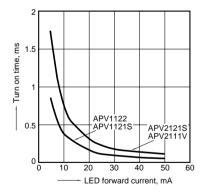


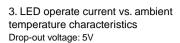
5. LED dropout voltage vs. ambient temperature characteristics LED forward current: 5 to 50mA

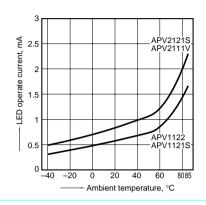


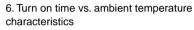
8. Turn on time vs. LED forward current characteristics

Load capacity: 1,000pF; output voltage: 5V

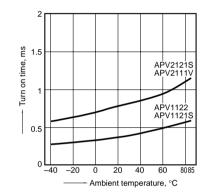




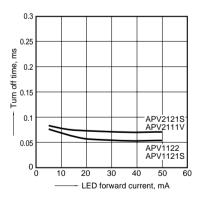




LED forward current: 10mA Load capacity: 1,000pF; output voltage: 5V



9. Turn off time vs. LED forward current characteristics Load capacity: 1,000pF; output voltage: 1V

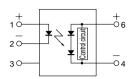


SCHEMATIC AND WIRING DIAGRAMS

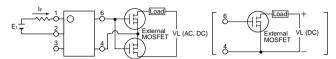
Notes: E1: Power source at input side; IF: LED forward current;

1. APV1122

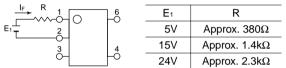
Schematic



Power MOSFET drive wiring diagram



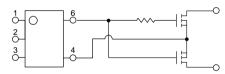
Example of each input power supply and current limit resistors ($I_F = 10mA$)



CAUTIONS FOR USE

1. When two external MOSFETs are connected with a common source terminal, oscillation may occur when operation is restored. Therefore, please insert a 100 to 1,000 ohms resistor between the gate terminal of the first MOSFET and the gate terminal of the second MOSFET.

A typical example of this is given in the circuit below.



2. Deterioration and destruction caused by discharge of static electricity

This phenomenon is generally called static electricity destruction, and occurs when static electricity generated by various factors is discharged while the relay terminals are in contact, producing internal destruction of the element. To prevent problems from static electricity, the following precautions and measures should be taken when using your device. 1) Employees handling relays should wear anti-static clothing and should be grounded through protective resistance of 500 k Ω to 1 M Ω .

2) A conductive metal sheet should be placed over the work table. Measuring instruments and jigs should be grounded.3) When using soldering irons, either use irons with low leakage current, or ground the tip of the soldering iron. (Use of lowvoltage soldering irons is also recommended.) 4) Devices and equipment used in assembly should also be grounded.
5) When packing printed circuit boards and equipment, avoid using high-polymer materials such as foam styrene, plastic, and other materials which carry an electrostatic charge.

6) When storing or transporting relays, the environment should not be conducive to static electricity (for instance, the humidity should be between 45 and 60%), and relays should be protected using conductive packing materials.

3. Unused terminals

The No. 3 terminal is used with the circuit inside the relay. Therefore, do not connect it to the external circuitry. (DIP 6-pin type)

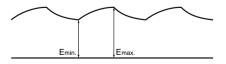
4. Short across terminals

Do not short circuit between terminals when relay is energized, since there is possibility of breaking of the internal IC.

5. Ripple in the input power supply 1) For LED operate current at Emin.

maintain min. 10 mA

2) Keep the LED operate current at 50 mA or less at E_{max} .

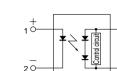


6. Soldering

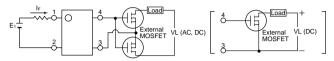
1) When soldering PC board terminals, keep soldering time less than 10 s at $260^{\circ}C$ 500°F.

2) When soldering surface-mount terminals, the following conditions are recommended.

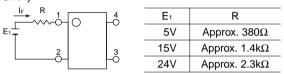
2. APV1121S, APV2121S, APV2111V Schematic



Power MOSFET drive wiring diagram



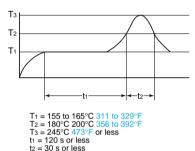
Example of each input power supply and current limit resistors ($I_F = 10mA$)



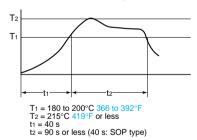
(1) IR (Infrared reflow) soldering method

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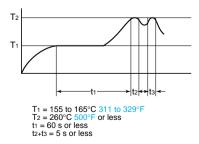
-03



(2) Vapor phase soldering method



(3) Double wave soldering method



(4) Soldering iron method Tip temperature: 280 to 300°C 536 to 572°F

Wattage: 30 to 60 W Soldering time: within 5 s

APV1,2

(5) Others

Check mounting conditions before using other soldering methods (hot-air, hot plate, pulse heater, etc.)

7. Important Notes for Mounting

1) Temperature rise in the lead portion is highly dependent on package size. If multiple different packages are mounted on the same board, please check your board beforehand in an actual product, ensuring that the temperature conditions of the phototriac coupler fall within the parameters listed above.

8. Cleaning

The phototriac coupler forms an optical path by coupling a light-emitting diode (LED) and photodiode via transparent silicon resin. For this reason, unlike other directory element molded resin products (e.g., MOS transistors and bipolar transistors), avoid ultrasonic cleansing if at all possible. We recommend cleaning with an organic solvent. If you cannot avoid using ultrasonic cleansing, please ensure that the following conditions are met, and check beforehand for defects.

- Frequency: 27 to 29 kHz
- Ultrasonic output:
- No greater than 0.25 W/cm²
- Cleaning time:
- No longer than 30 s
- Cleanser used: Asahiklin AK-225
- Other: Submerge in solvent in order to prevent the PCB and elements from being contacted directly by the ultrasonic vibrations.
- Note: Applies to unit area ultrasonic output for ultrasonic baths.

9. Storage

SSOP package (APV2111V) and SO package (APV1121S, APV2121S) are sensitive to moisture and come in sealed moisture-proof packages. Observe the following cautions on storage.

- After the moisture-proof package is unsealed, take the devices out of storage as soon as possible (within 1 month at the most).
- If the devices are to be left in storage for a considerable period after the moistureproof package has been unsealed, it is recommended to keep them in another

moisture-proof bag containing silica gel (within 3 months at the most).

Note: When thermal stress is applied when solder mounting under moist conditions, water will evaporate, dilation will occur, the stress inside of the package will increase, and swelling and cracking might occur on the package surface. Therefore, please be careful while following the soldering conditions given on the next page.

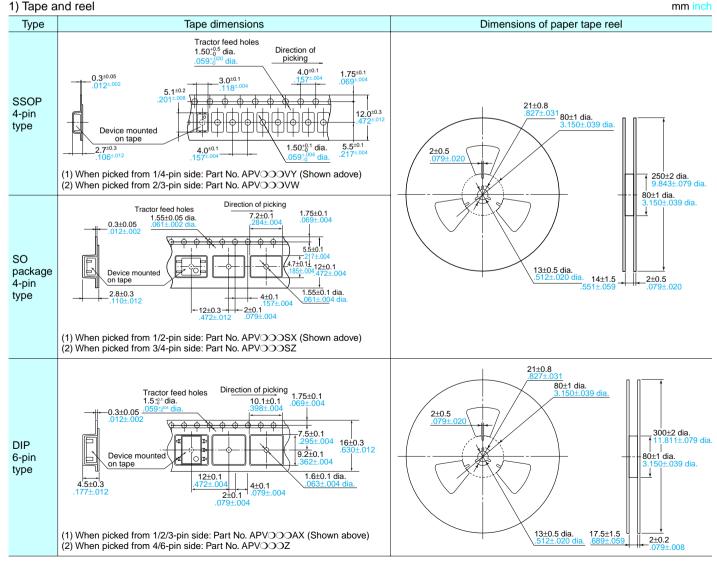
10. Transportation and storage

1) Extreme vibration during transport will warp the lead or damage the relay. Handle the outer and inner boxes with care.

2) Storage under extreme conditions will cause soldering degradation, external appearance defects, and deterioration of the characteristics. The following storage conditions are recommended:

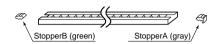
- Temperature: 0 to 45°C 32 to 113°F
- Humidity: Less than 70% R.H.
- Atomosphere: No harmful gasses such as sulfurous acid gas, minimal dust.





2) Tube

(1) MOSFET driver is packaged in a tube so pin No. 1 is on the stopper B side.
Observe correct orientation when mounting them on PC boards.
(SOP type)



(DIP type)



12. Applying stress that exceeds the absolute maximum rating

If the voltage or current value for any of the terminals exceeds the absolute maximum rating, internal elements will deteriorate. In extreme cases, wiring may melt, or silicon P/N junctions may be destroyed.

As a result, the design should ensure that the absolute maximum ratings will never be exceeded, even momentarily.

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These materials are printed on ECF pulp.

These materials are printed with earth-friendly vegetable-based (soybean oil) ink.



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