

7865/7/97



-Timing

Solid-State **Cube Timers**

Delay on Make (Series Load) **1D Series**

Allows user to accurately set delay times up to 1023 seconds without time consuming trial and error adjustments.

Operating Logic: Upon application of input voltage, the delay starts. At the end of the time delay, the load is energized. Reset is accomplished by removing input voltage. Set the delay time by switching the appropriate combination of time values to the "ON" position.

Note: The load may be located on either side of the line

Protection

Transient Voltage: 30 Joule Metal oxide varistor

- Dielectric Breakdown: 3000 VAC, RMS, terminals to mounting
- Insulation Resistance: 100 megohms minimum between terminals and case

Mechanical

Termination: .25" x 032" male fast-on terminals Mounting: Surface mount with one #8 screw

Environmental

Storage Temperature: -40°C to 85°C **Operating Temperature:** -40°C to 65°C **Humidity:** 95% relative

> Switch Setting Example: ON



Time Selected= 55 sec.

Ordering Information

Input Voltage

Load

temperature.

be full-wave rectified)

On Off

On

Off

Specifications

ments. Zero time setting is undefined.

On DC models, unfiltered supply voltage must

Time Range		24 to 240 VAC/DC \pm 10%
1 - 1023 Seconds		Q1D-01024-31M
Reset: During Timing		125 mS
Reset: After Time Out		10 mS
Min. Load		10 mA or 1 VA whichever is greater
Max. Leakage Current		2 mA
Voltage Drop @ 1A		3.3 V Max.
Power Consumption	During Timing	0.25 VA Max.
	After Time Out	3.3 VA
Peak One Cycle Surge		20 Amp
Protection		30j. MOV

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