

Applications / Markets







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SPECIFICATIONS

ANTI-VANDAL SWITCHES

DETECTOR SWITCHES

DIP SWITCHES

KEYLOCK SWITCHES

NAVIGATION SWITCHES

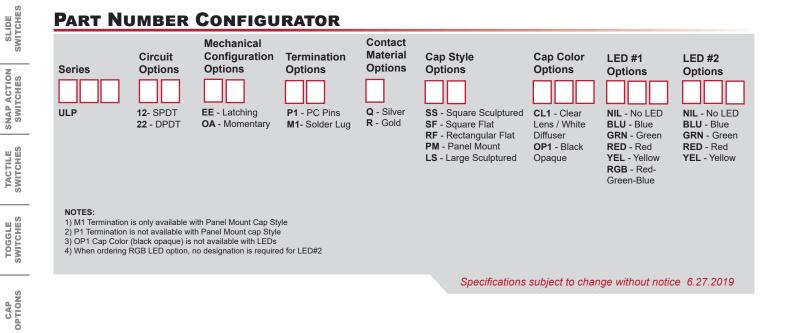
PUSHBUTTON SWITCHES

ROCKER SWITCHES

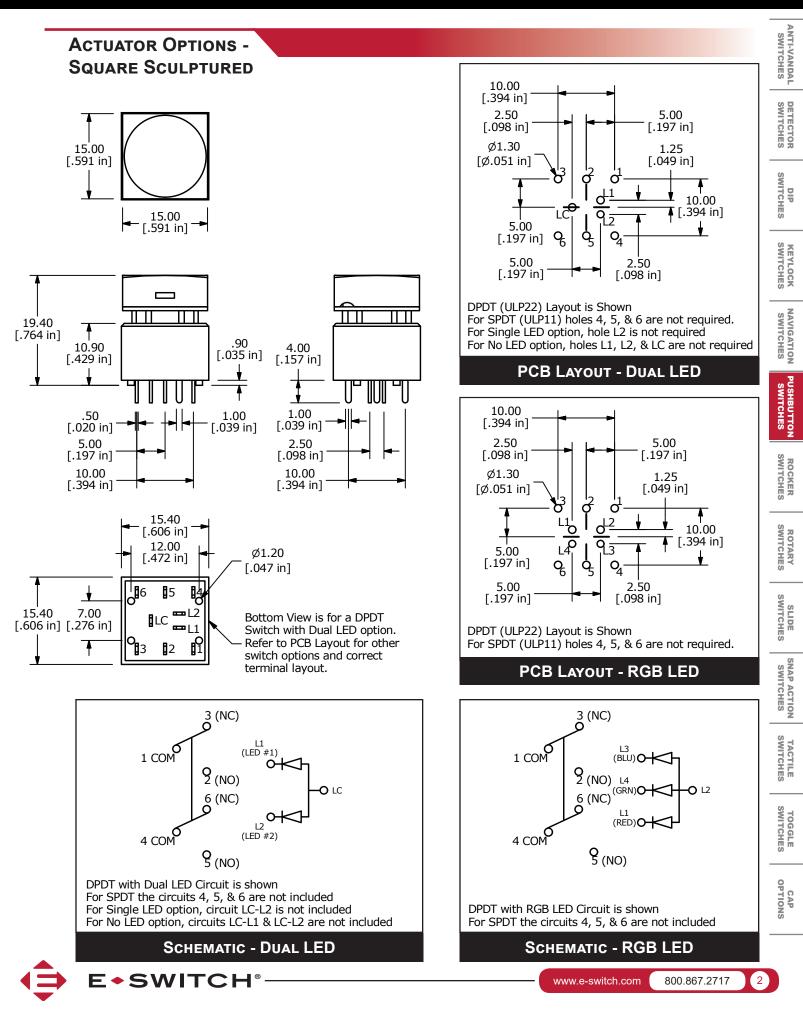
ROTARY SWITCHES Electrical Rating: Silver: .5A @ 25VDC Gold: 0.1A Max @ 28VDC Electrical Life: 200,000 Cycles Contact Resistance: Silver: $50m\Omega$ Max. initial Gold: $100m\Omega$ Max. initial Insulation Resistance: 100 M Ω Min. @ 500VDC Dielectric Strength: 1,000VAC Operating/Storage Temperature: -40° C to 85° C Travel: 2.20mm Contact Arrangement: SPDT, DPDT Actuation Force: SPDT: 200gf; DPDT: 300 gf Panel Thickness: 0.75-3.0mm (panel mount version only)

FEATURES & BENEFITS

- Single pole or double pole functions
- Multiple color options including RGB color
- Multiple cap color and style options
- PC pin or solder lug termination
- Latching or momentary options
- Gold or silver contact material



1



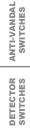


.90

[.035 in]

1.00

[.039 in]



DIP SWITCHES

KEYLOCK SWITCHES

NAVIGATION SWITCHES

PUSHBUTTON SWITCHES

ROCKER SWITCHES

ROTARY SWITCHES

SUIDE SWITCHES

SNAP ACTION SWITCHES

TACTILE SWITCHES

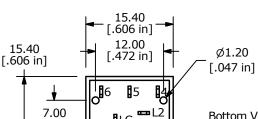
TOGGLE SWITCHES

CAP OPTIONS

1 COM 4 COM

Bottom View is for a DPDT Switch with Dual LED option. Refer to PCB Layout for other switch options and correct terminal layout.

O LC



3 (NC) O

9_(NO)

6 (NC)

₿ (NO)

For No LED option, circuits LC-L1 & LC-L2 are not included

SCHEMATIC - DUAL LED

For SPDT the circuits 4, 5, & 6 is not included For Single LED option, circuit LC-L2 are not included

DPDT with Dual LED Circuit is shown

E+SWITCH[®]

L1 (LED #1)

o

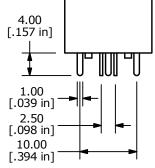
L2

(LED #2)

LC

2

13



SQUARE FLAT 12.00

[.472 in]

10.90

[.429 in]

.50

[.020 in]

5.00

[.197 in]

10.00

[.394 in]

[.276 in]

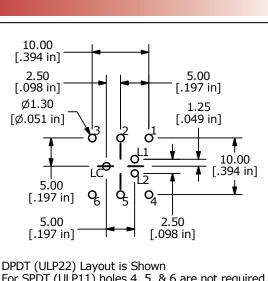
4

13.60 [.535 in]

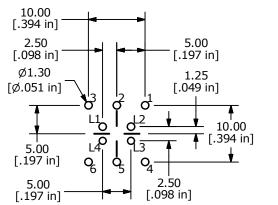
ACTUATOR OPTIONS -

12.00

[.472 in]



[.098 in] [.197 in] DPDT (ULP22) Layout is Shown



PCB LAYOUT - DUAL LED

For SPDT (ULP11) holes 4, 5, & 6 are not required. For Single LED option, hole L2 is not required For No LED option, holes L1, L2, & LC are not required

SCHEMATIC - RGB LED

DPDT with RGB LED Circuit is shown For SPDT the circuits 4, 5, & 6 are not included

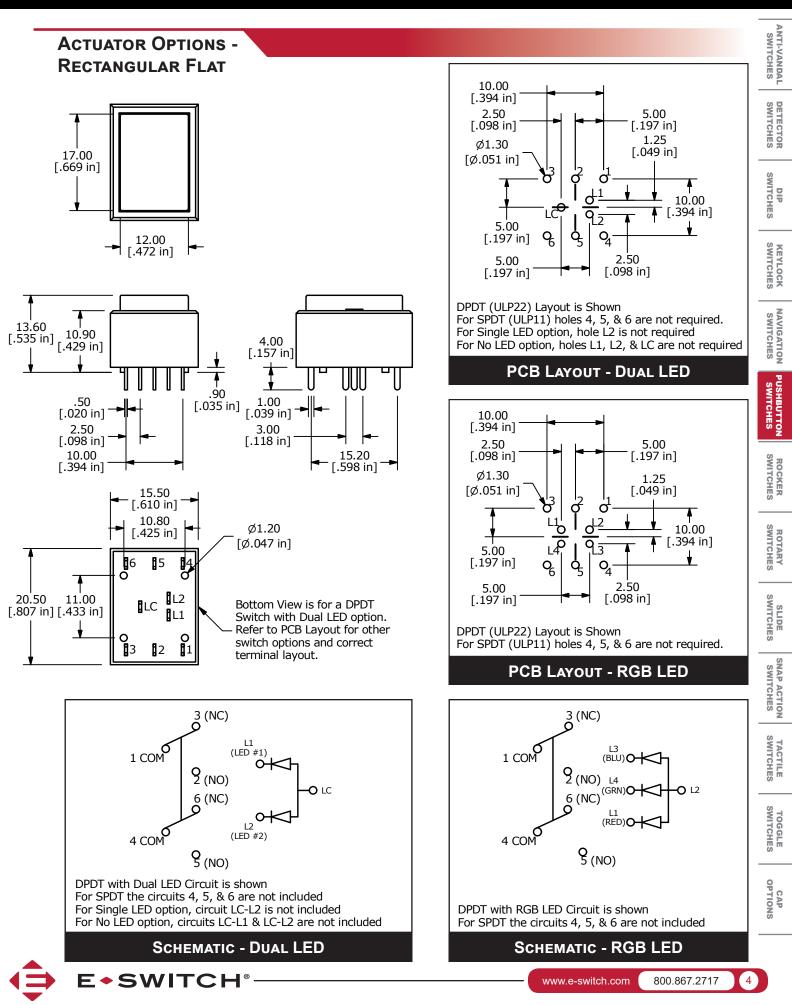
3 (NC) 1 COM 2 (NO) 6 (NC) (GRN)O O L2 (RED)O-4 COM 8 (NO)

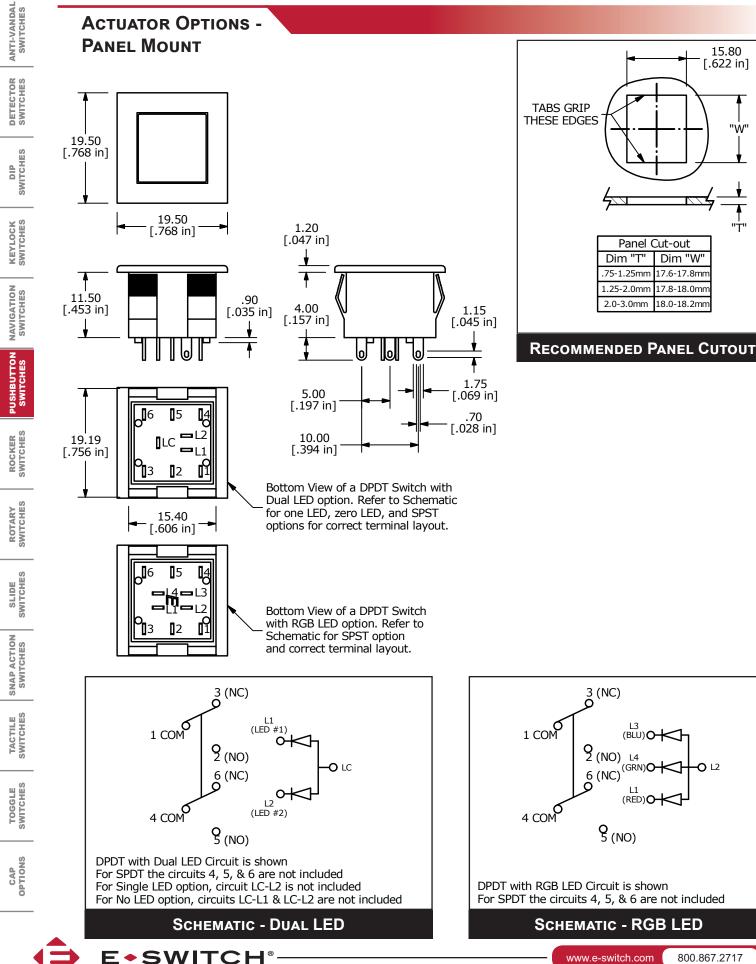
PCB LAYOUT - RGB LED

For SPDT (ULP11) holes 4, 5, & 6 are not required.

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3 800.867.2717





800.867.2717

5

O L2

15.80 [.622 in]

"W"

"†"

