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FEATURES

- C/MOS Digital Circuitry
- · Time Delays to 1000 Minutes
- No First Cycle Effect
- · Fully Solid State and Encapsulated
- 0.5% Repeat Accuracy
- Output Rated at 1 Ampere Continuous, 10 Amperes Inrush
- · Fixed, Local, or Externally Adjustable Time Delays
- Small Size
- UL/cUL Recognized

SPECIFICATIONS

1. Time Delay

- 1.1 Type: C/MOS Digital Circuitry
- 1.2 Range: From 0.05 Seconds to 1000 Minutes

Fixed Delays Available(See Time Delay Chart)

- 1.3 Repeat Accuracy: ±0.5% Under Fixed Conditions
- 1.4 Setting Accuracy: ±10%
- 1.5 Reset Time: 50 Milliseconds Maximum
- 1.6 Recycle Time: 100 Milliseconds During Timing

50 Milliseconds After Timing

1.7 Time Delay vs Voltage and Temperature: ±2%

2. Input

- 2.1 Operating Voltage
- 2.2 Tolerance: ±20% of Nominal
- 2.3 Frequency: 50-60 Hertz

3. Output

- 3.1 Type: Solid State
- 3.2 Form SPST
- 3.3 Rating: 1 Amp Steady State (10 Amp Inrush, 20mA Min.)
- 3.4 Life: 100,000,000 Operations Minimum Under Full Load

4. Protection

- 4.1 Transient: ±1500 Volts for 150 Microseconds
- 4.2 Polarity: DC Units Are Reverse Polarity Protected
- 4.3 Dielectric Breakdown: 1500 Volts RMS Minimum

5. Mechanical

- 5.1 Mounting: One #8 or #10 Screw
- 5.2 Termination: 1/4" Quick Connect Terminals
- 5.3 Style: Surface Mount Encapsulated

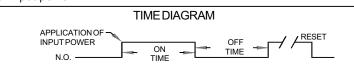
6. Environmental

- 6.1 Operating Temperature: -20°C to +80°C
- 6.2 Storage Temperature: -30°C to +85°C
- 6.3 Humidity: 95% Relative Non-Condensing

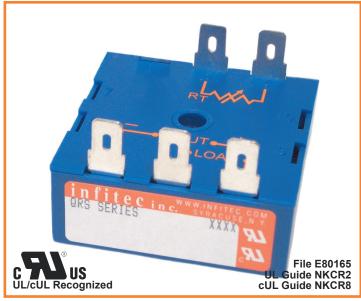
MODE OF OPERATION

ON/OFF RECYCLE

Upon application of power to the input terminals, the ON delay begins and the output contact transfers. Upon completion of the ON delay, the output contact reverts back to its original position and the OFF delay begins. Upon completion of the OFF delay, the output contact again transfers and the cycle repeats. Reset is accomplished by removal of input power.

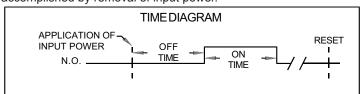


QRS SERIES DIGITAL ENCAPSULATED RECYCLING TIME DELAY MODULE

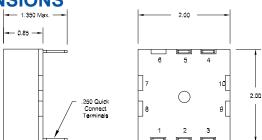


OFF/ON RECYCLE

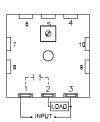
Upon application of power to the input terminals, the OFF delay begins. Upon completion of the OFF delay, the output contact transfers and the ON delay begins. Upon completion of the ON delay, the output contact reverts to its original position and the cycle repeats. Reset is accomplished by removal of input power.

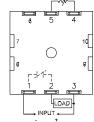


DIMENSIONS



CONNECTION DIAGRAMS





Local Adjustment Shown

External Adjustment Shown

ORDERING INFORMATION				
SERIES	INPUT	ADJUSTMENT	CYCLE	TIME DELAY
QRS	1 - 12 VDC 2 - 24/28 VDC 4 - 24 VAC 5 - 120 VAC 6 - 230 VAC	0 - Local Adjust 1 - Fixed 2 - External Adjust	1 - On Time First 2 - Off Time First	See Time Delay Range Chart