

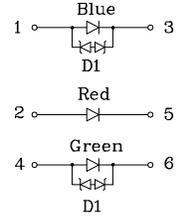
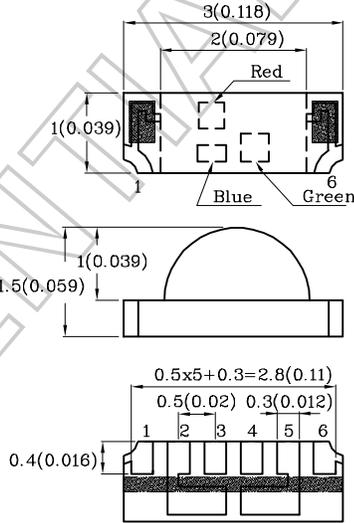
Features

- 3.0 x 1.0 x 1.5 mm right angle SMD LED
- Ideal for indication on hand held products
- Low current operation
- Standard Package: 2,000pcs/ Reel
- MSL (Moisture Sensitivity Level): 3
- Halogen-free
- RoHS compliant



ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELECTROSTATIC
DISCHARGE
SENSITIVE
DEVICES

Package Schematics



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.2(0.008)$ " unless otherwise noted.
3. Specifications are subject to change without notice.

Part Number	Emitting Color	Emitting Material	Lens-color	Luminous Intensity CIE127-2007* ($I_F=5mA$) mcd		I_v (mcd) [2] @B:R:G=3.9mA: 5mA:3.8mA	Dice Chromaticity Coordinates	Wavelength CIE127-2007* nm ΔP	Viewing Angle 20 1/2
				min.	typ.				
XZC157W-CC32	Blue	InGaN	Water Clear	30*	54*	357*	0.3	0.3	465*
	Red	AlGaInP		50*	98*				640*
	Green	InGaN		180*	278*				520*

*Luminous intensity value and wavelength are in accordance with CIE127-2007 standards.

Electrical / Optical Characteristics at T_A=25°C

Parameter	Condition	Symbol	Value			Unit
			Blue	Red	Green	
Wavelength at Peak emission CIE127-2007*	I _F =5mA	λ _{peak}	465*	640*	520*	nm
Dominant Wavelength CIE127-2007*	I _F =5mA	λ _{dom}	470*	625*	525*	nm
Spectral bandwidth at 50%Φ _{REL MAX}	I _F =5mA	Δλ	22	20	35	nm
Capacitance	I _F =5mA	C	100	27	100	pF
Forward Voltage	I _F =5mA	V _F [typ.]	2.8	1.9	2.9	V
		V _F [max.]	3.2	2.2	3.3	
Reverse Current	V _R =5V	I _R [max.]	50	10	50	μA
Temperature Coefficient of λ _{peak} , -10°C ≤ T ≤ 85°C	I _F =5mA	TC _{λ_{peak}}	0.04	0.13	0.05	nm/°C
Temperature Coefficient of λ _{dom} , -10°C ≤ T ≤ 85°C	I _F =5mA	TC _{λ_{dom}}	0.03	0.06	0.03	nm/°C
Temperature Coefficient of V _F , -10°C ≤ T ≤ 85°C	I _F =5mA	TC _V	-3.0	-2.0	-3.0	mV/°C

Note:

* Wavelength value is in accordance with CIE127-2007 standards.

Absolute Maximum Ratings at T_A=25°C

Parameter	Symbol	Value			Unit
		Blue	Red	Green	
Power dissipation	P _D	120	84	120	mW
Reverse Voltage	V _R	5	5	5	V
Junction Temperature	T _J	115	115	115	°C
Operating Temperature	T _{op}	-40 To +85			°C
Storage Temperature	T _{stg}	-40 To +85			°C
DC Forward Current	I _F	30	30	30	mA
Peak Forward Current	I _{FM} ^[1]	100	150	100	mA
Electrostatic Discharge Threshold (HBM)		8000	3000	8000	V
Thermal Resistance (Junction / Ambient)	R _{th JA} ^[2]	500	520	680	°C/W
Thermal Resistance (Junction / Solder point)	R _{th JS} ^[2]	390	440	570	°C/W

Notes:

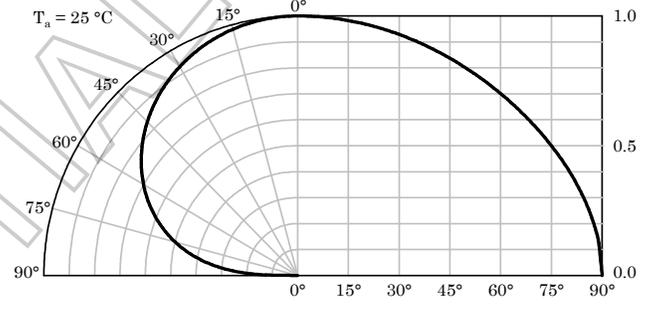
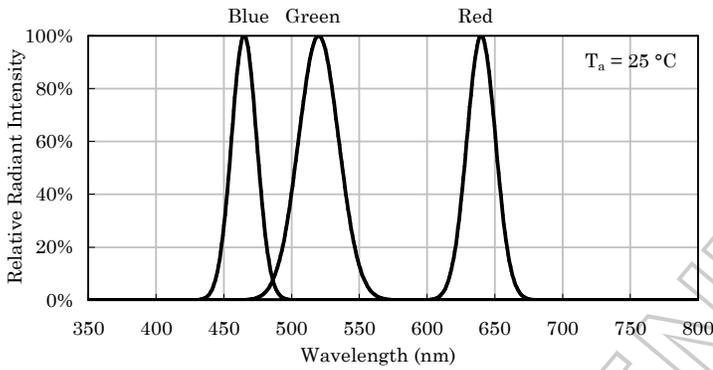
1. 1/10 Duty Cycle, 0.1ms Pulse Width.

2. R_{th JA}, R_{th JS} Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad).

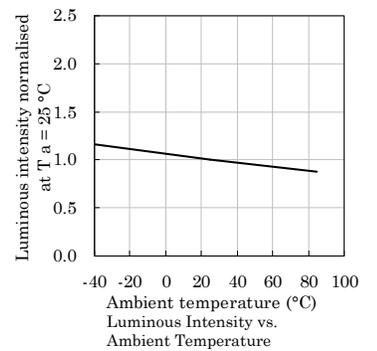
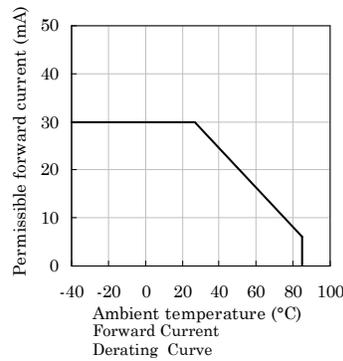
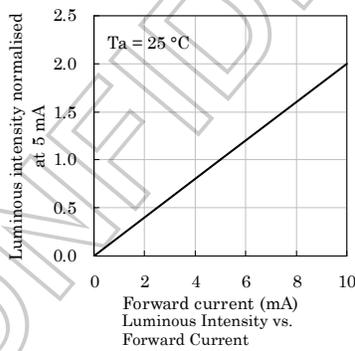
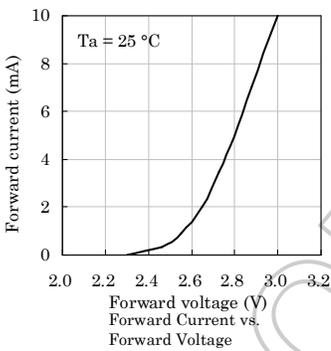
3. A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

Jul 31, 2021

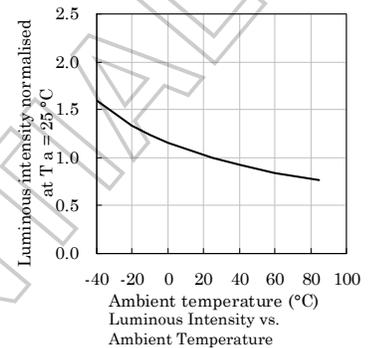
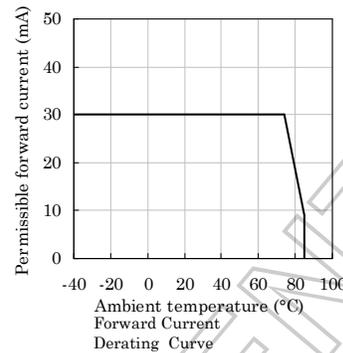
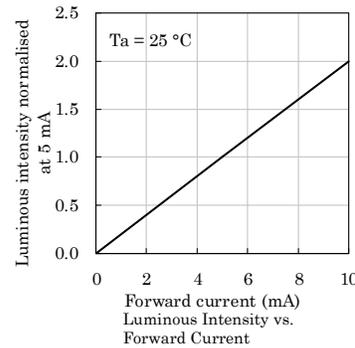
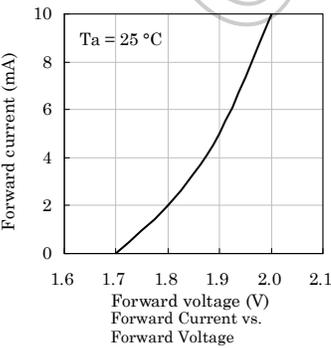
XDSB9461 V1-Z Layout: Maggie L.



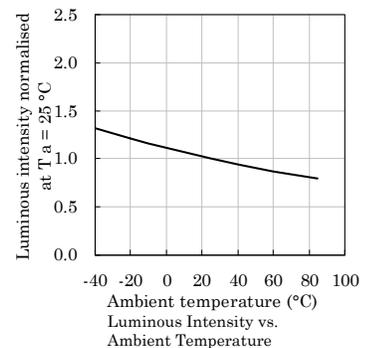
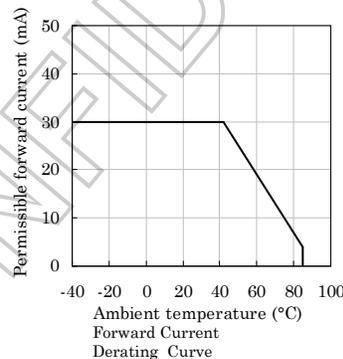
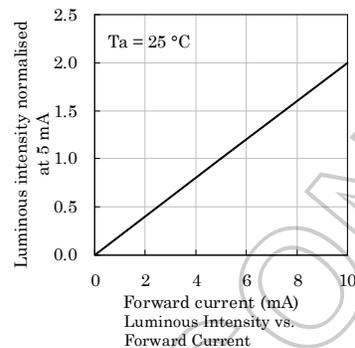
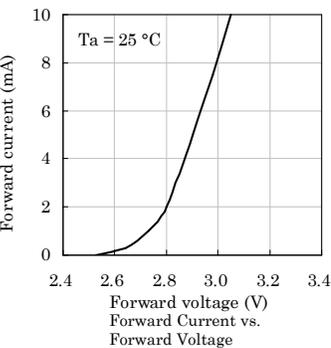
❖ Blue



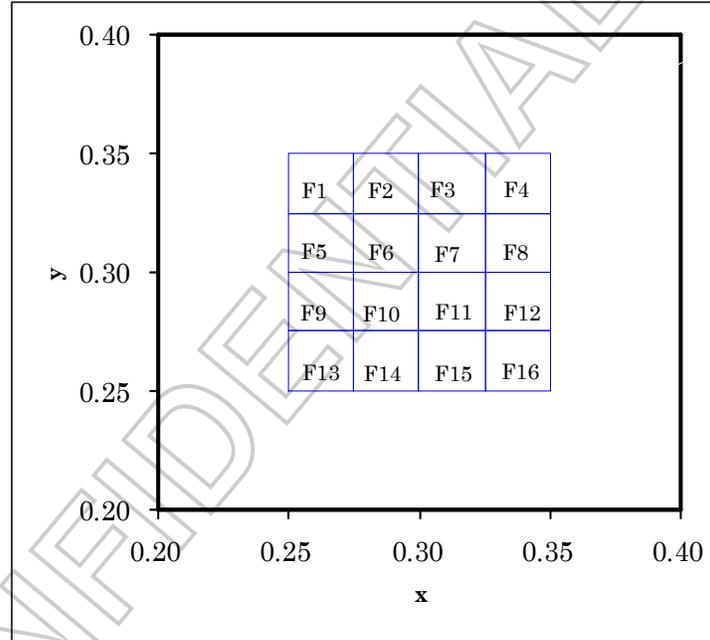
❖ Red



❖ Green



Full color (White Rank)

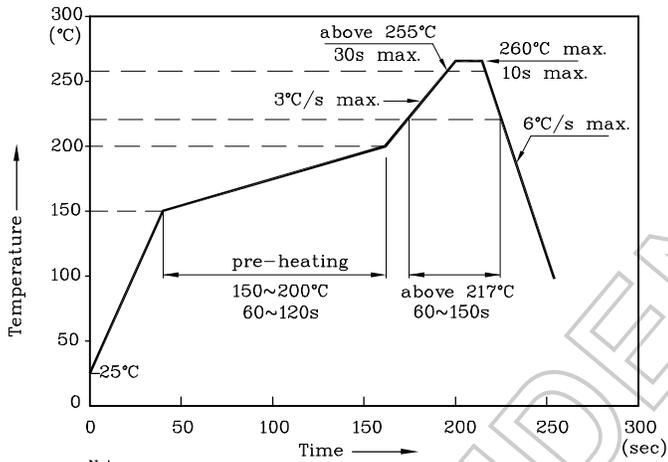


F1		F2		F3		F4	
x	y	x	y	x	y	x	y
0.250	0.325	0.275	0.325	0.300	0.325	0.325	0.325
0.275	0.325	0.300	0.325	0.325	0.325	0.350	0.325
0.275	0.350	0.300	0.350	0.325	0.350	0.350	0.350
0.250	0.350	0.275	0.350	0.300	0.350	0.325	0.350
F5		F6		F7		F8	
x	y	x	y	x	y	x	y
0.250	0.300	0.275	0.300	0.300	0.300	0.325	0.300
0.275	0.300	0.300	0.300	0.325	0.300	0.350	0.300
0.275	0.325	0.300	0.325	0.325	0.325	0.350	0.325
0.250	0.325	0.275	0.325	0.300	0.325	0.325	0.325
F9		F10		F11		F12	
x	y	x	y	x	y	x	y
0.250	0.275	0.275	0.275	0.300	0.275	0.325	0.275
0.275	0.275	0.300	0.275	0.325	0.275	0.350	0.275
0.275	0.300	0.300	0.300	0.325	0.300	0.350	0.300
0.250	0.300	0.275	0.300	0.300	0.300	0.325	0.300
F13		F14		F15		F16	
x	y	x	y	x	y	x	y
0.250	0.250	0.275	0.250	0.300	0.250	0.325	0.250
0.275	0.250	0.300	0.250	0.325	0.250	0.350	0.250
0.275	0.275	0.300	0.275	0.325	0.275	0.350	0.275
0.250	0.275	0.275	0.275	0.300	0.275	0.325	0.275

Notes:
 Shipment may contain more than one chromaticity regions.
 Orders for single chromaticity region are generally not accepted.
 Measurement tolerance of the chromaticity coordinates is ± 0.01 .

❖ LED is recommended for reflow soldering and soldering profile is shown below.

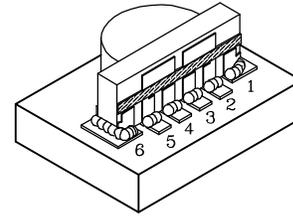
Reflow Soldering Profile for SMD Products (Pb-Free Components)



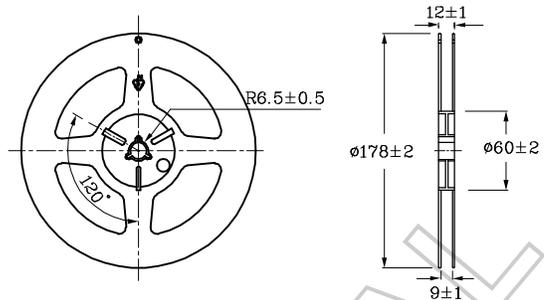
Notes:

1. All temperatures refer to the center of the package, measured on the package body surface facing up during reflow.
2. Do not apply any stress to the LED during high temperature conditions.
3. Maximum number of soldering passes: 2

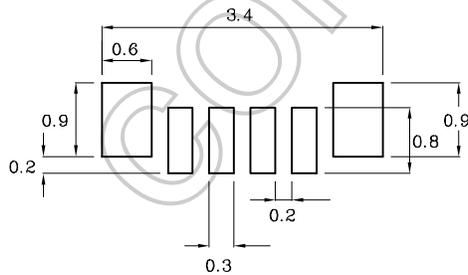
❖ The device has a single mounting surface. The device must be mounted according to the specifications.



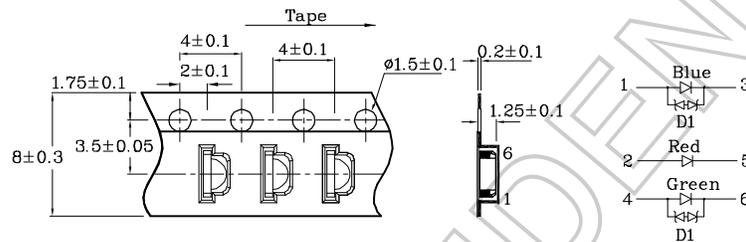
❖ Reel Dimension (Units : mm)



❖ Recommended Soldering Pattern (Units : mm; Tolerance: ± 0.1)



❖ Tape Specification (Units : mm)



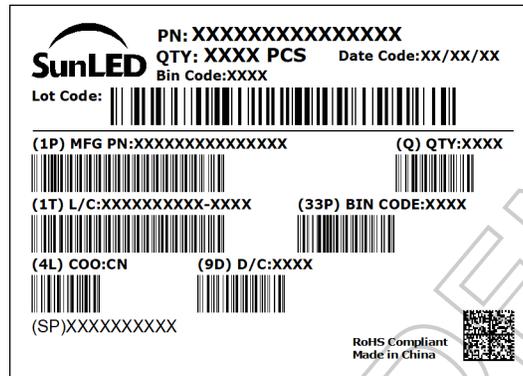
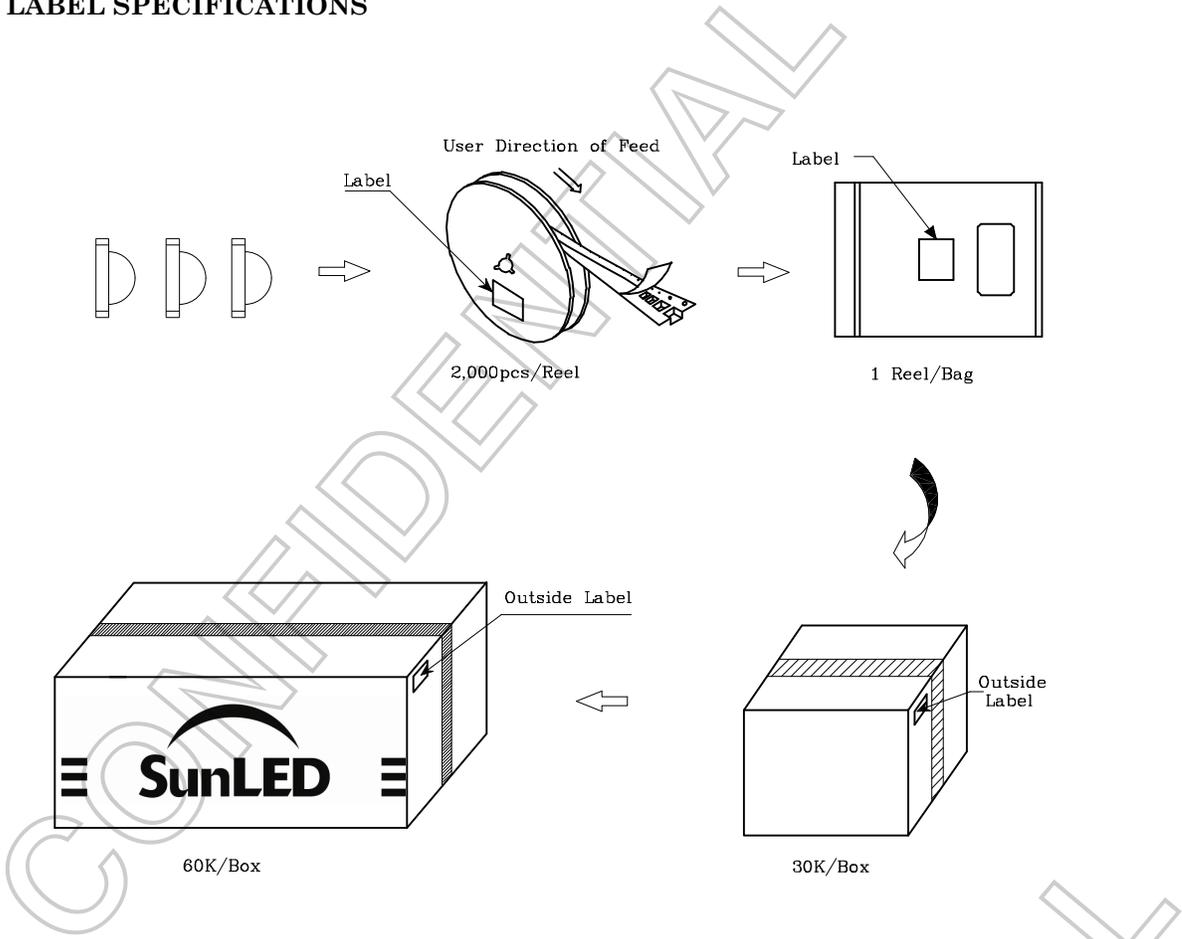
Remarks:

If special sorting is required (e.g. binning based on forward voltage, Luminous intensity / luminous flux, or wavelength), the typical accuracy of the sorting process is as follows:

1. Wavelength: +/-1nm
2. Luminous intensity / luminous flux: +/-15%
3. Forward Voltage: +/-0.1V

Note: Accuracy may depend on the sorting parameters.

PACKING & LABEL SPECIFICATIONS



TERMS OF USE

1. Data presented in this document reflect statistical figures and should be treated as technical reference only.
2. Contents within this document are subject to improvement and enhancement changes without notice.
3. The product(s) in this document are designed to be operated within the electrical and environmental specifications indicated on the datasheet. User accepts full risk and responsibility when operating the product(s) beyond their intended specifications.
4. The product(s) described in this document are intended for electronic applications in which a person's life is not reliant upon the LED. Please consult with a SunLED representative for special applications where the LED may have a direct impact on a person's life.
5. The contents within this document may not be altered without prior consent by SunLED.
6. Additional technical notes are available at <https://www.SunLEDusa.com/TechnicalNotes.asp>