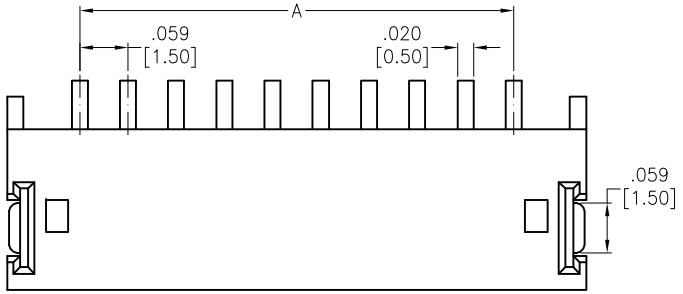
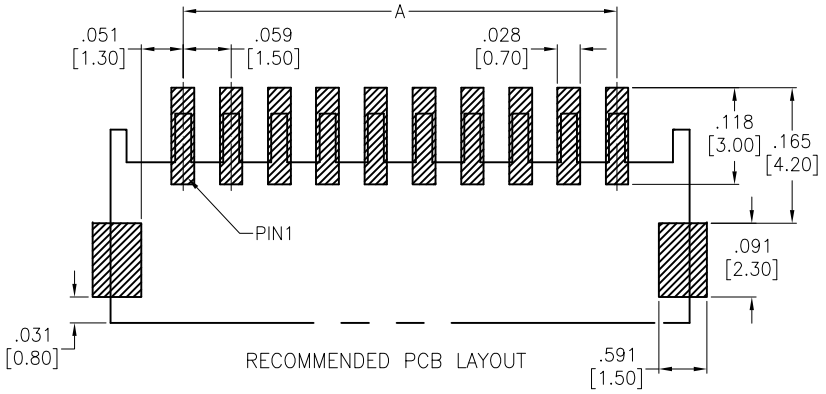
Rev	AWO #	Description	Date	Appr
-		RELEASED	5/11/20	

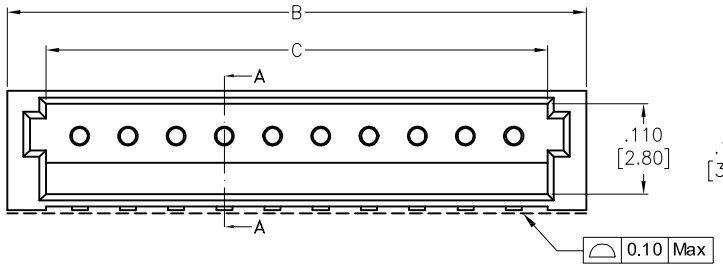
APPROVED: _____

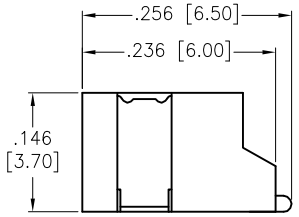
DATE: _____ TITLE: _____

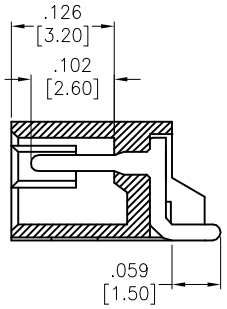




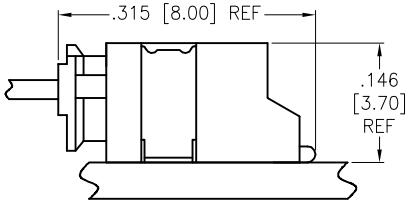
RECOMMENDED PCB LAYOUT







SECTION A–A



ASSEMBLY LAYOUT

SPECIFICATIONS:

Material:
Insulator: Hi–Temp Thermoplatic, rated UL 94V–0
Insulator color: Natural
Contacts: Brass
Plating:
Tin over Nickel underplate overall
Electrical:
Voltage rating: 50 V AC/DC
Current rating: 1A AC/DC
Contact resistance: 20 mohms Max
Insulation resistance: 500 Mohms min
Dielectric withstanding voltage: 500 VAC for 1 minute
Temperature Rating:
–25°C to +85°C
Environmental:
Lead free, RoHS compliant.

RoHS COMPLIANT

PACKAGED ON TAPE & REEL
Positions: 2 thru 15
A = .059 [1.50] x No. of Spaces
B = .059 [1.50] x No. of Spaces + .177 [4.50]
C = .059 [1.50] x No. of Spaces + .083 [2.10]

UNLESS OTHERWISE SPECIFIED
ALL DIMENSIONS ARE INCHES [MM]
TOLERANCES: EXCEPT AS NOTED

INCHES		HOLES	
.X	± .10	Ø.X	±.10
.XX	± .020	Ø.XX	±.015
.XXX	± .015	Ø.XXX	±.010

APPROVALS		DATE
DRAWN	AY	5/11/20
CHECKED	LW	5/11/20
APPROVED		

ADAM TECH INTERCONNECTS
909 Rahway Avenue,
Union, NJ 07083
Phone: 908-687-5000
Fax: 908-687-5710

TITLE
.059 [1.50] WAFER ,RIGHT ANGLE, SMT TYPE
TIN PLATED

SIZE	PART NO.	REV.
X	15SH-A6-XX-TR-SMT-T/R	-

REF:	SCALE:	SHEET
S00124T	NTS	1 OF 1