1 Description

The RedRock® RR122-3C62-511 and RR122-3C63-511 digital magnetic sensors are ideal for use in medical, industrial, automotive, and consumer applications. They are based on patented Tunneling Magnetoresistance (TMR) technology with seamless CMOS integration.

The RR122-3C62-511 and RR122-3C63-511 feature an operate sensitivity of 10 G (1.0 mT) with a bipolar magnetic field response. They offer a wide supply voltage range from 1.7V up to 5.5V, ideal for applications ranging from small battery-powered electronics to industrial machinery. They have a low average current drain (1.7 µA) for an active magnetic sensor, operating at 500 Hz and high operating temperature ranges from -40°C up to 125°C.

2 Features

- ▶ Operate sensitivity of 10 G
- ► Lowest Average Current < 1.7 µA
- ▶ Wide Supply Voltage Range of 1.7V 5.5V
- ▶ Bipolar Latching Push-Pull Response
- ▶ Operating Frequency of 500 Hz
- ► Temperature Rated up to 125°C
- ► RoHS & REACH Compliant

3 Applications

- ▶ Proximity Detection
- ► Linear Position Sensing
- ► Rotary Sensing
- ▶ Fluid Level Detection
- ▶ Door & Lid Closure Detection
- ► Utility Meters
- ▶ Portable Medical Devices
- ► Motor Controllers
- ► Consumer Electronics

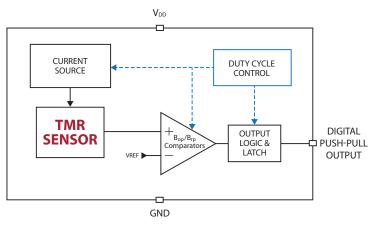
Device Information

| Part Series | Package | Body Size (mm) | Temp Rating °C |
|----------------|----------|-----------------|----------------|
| RR122-3C62-511 | SOT-23-3 | 2.9 x 1.6 x 1.2 | -40 – +85 |
| RR122-3C63-511 | SOT-23-3 | 2.9 x 1.6 x 1.2 | -40 – +125 |

Device Package



Functional Block Diagram for Digital Push-Pull Output



Device Nomenclature

Ordering Information

RR122-X X X X-Y Y Y Series Magnetic Polarity Response 1 = Omnipolar3 = Bipolar/Latch 2 = Unipolar Magnetic Sensitivity (G) A: Op 9, Rel 5 E: Op 15, Rel 10 B: Op 30, Rel 20 F: Op 70, Rel 50 C: Op 10, Rel -10 Clock Frequency (Hz)

1: 2 5: 250 9: 10000 2: 10 6: 500 U: 100+Turbo (10000)

Package 1: SOT-23-3 2: LGA-4 5: SOT-23-5 **Output Response** 1: Active Low Supply Voltage (V) 5: 1.7 - 5.5 Temp Rating (°C)

2: -40 - +85 3: -40 - +125

3: 20 7: 2500



4 Specifications

4.1 Absolute Environmental Ratings^{1,2}

| Parameters | Units | Min | Тур | Max |
|---|-------|-------|-----|------|
| Operating Temperature (T _{OP}) (RR122-3C62-511) | °C | -40 | | +85 |
| Operating Temperature (T _{OP}) (RR122-3C63-511) | ۰C | -40 | | +125 |
| Storage Temperature (T _{STG}) | ۰C | -65 | | +150 |
| Junction Temperature (T _J) | ۰C | | | +150 |
| Soldering Temperature (3 cycles, 1 min.) (T _{sol.}) | ۰C | | | +260 |
| ESD Level Human Body Model per JESD22-A114 | V | ±4000 | | |
| Junction-to-Ambient Thermal Resistance | °C/W | | 215 | |

4.2 Absolute Electrical Ratings^{1,2}

| Parameters | Units | Min | Тур | Max |
|---|-------|------|-----|----------|
| Supply Voltage (V _{DD}) | V | -0.3 | | 6.0 |
| Push-pull Output (Active Low)(V _{OUT_PP}) | V | -0.3 | | V_{DD} |
| Input and Output Current (I _{IN} /I _{OUT}) | mA | -10 | | 10 |

Notes:

- 1. Exceeding Absolute Ratings may cause permanent damage to the device. Exposure at the maximum rated conditions for extended periods of time may also affect device reliability.
- 2. Unless otherwise specified, all characteristics are measured at 25°C.



ESD Note: This product uses semiconductors that can be damaged by electrostatic discharge (ESD). When handling, proper ESD precautions should be taken to avoid performance degradation or loss of functionality. Damage due to inappropriate handling is not covered under warranty.





4 Specifications (cont.)

4.3 Operating Electrical Characteristics for RR122-3C62-511 and RR122-3C63-5111

| Parameters | Units | Min | Тур | Max |
|---|-------|---------------------|------|----------------|
| Supply Voltage (V _{DD}) | V | 1.7 | | 5.5 |
| Power-On Time $(t_{ON})(V_{DD} > 90\%)$ | μs | | 50 | 75 |
| Peak Power-On Current | mA | | | 1.4 |
| Active Mode Time (t _{ACT}) | μs | | 1.4 | |
| Output Voltage (High) (V _{OUTH}) | V | 90% V _{DD} | | |
| Output Voltage (Low) (V _{OUTL}) | V | | | $10\%\ V_{DD}$ |
| Under Voltage Lockout Threshold Rising V _{DD} (V _{UVLO-RISE}) | V | | 1.6 | 1.65 |
| Under Voltage Lockout Threshold Falling V _{DD} (V _{UVLO-FALL}) | V | 1.40 | 1.55 | |
| Under Voltage Lockout Hysteresis (V _{UV-HYST}) | mV | | 50 | |
| Average Supply Current @ $V_{DD} = 1.7 \text{ V, } f_s = 500 \text{ Hz}^2$ | μΑ | | 1.7 | _ |
| Average Supply Current @ $V_{DD} = 3.0 \text{ V}$, $f_s = 500 \text{ Hz}^2$ | μΑ | | 1.9 | |

Notes:

- 1. Unless otherwise specified, $V_{DD} = 1.7 \text{ V}$ to 5.5 V, $T_A = -40 ^{\circ}\text{C}$ to $+85 ^{\circ}\text{C}$ (3C62), $-40 ^{\circ}\text{C}$ to $+125 ^{\circ}\text{C}$ (3C63). Typical values are $V_{DD} = 3.0 \text{ V}$ and $T_A = +25 ^{\circ}\text{C}$.
- 2. Conditions: t = 10 seconds



ESD Note: This product uses semiconductors that can be damaged by electrostatic discharge (ESD). When handling, proper ESD precautions should be taken to avoid performance degradation or loss of functionality. Damage due to inappropriate handling is not covered under warranty.

4.4 Operating Characteristics for RR122-3C62-511 and RR122-3C63-5111

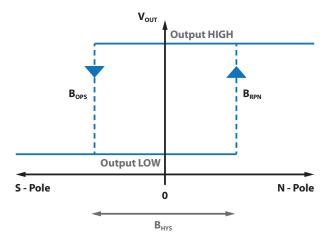
| Parameters | Units | Min | Тур | Max |
|--|-------|-----|-----|-----|
| Switching Frequency (f _{sw}) | Hz | | 500 | |
| Active Mode Time (t _{ACT}) | μs | | 1.4 | |
| Idle Mode Time (t _{IDLE}) | ms | | 2 | |
| Operate Point (B _{OPS}) | G | -14 | -10 | -8 |
| Release Point (B _{RPN}) | G | 8 | 10 | 14 |

Notes:

1. Unless otherwise specified, $V_{DD} = 1.7 \text{ V}$ to 5.5 V, $T_A = -40 ^{\circ}\text{C}$ to $+85 ^{\circ}\text{C}$ (3C62), $-40 ^{\circ}\text{C}$ to $+125 ^{\circ}\text{C}$ (3C63). Typical values are $V_{DD} = 3.0 \text{ V}$ and $T_A = +25 ^{\circ}\text{C}$.



5 Output Response vs. Magnetic Flux

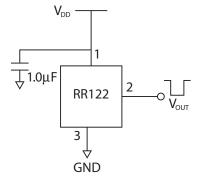


6 Application Information

RR122 Push-Pull Output Application Circuit

The output voltage on the RR122 is active low, meaning that while a sufficiently strong magnetic field is present, the output voltage is low. The output voltage can be connected to a digital I/O pin on a microcontroller. A decoupling capacitor with a minimum value of 1.0 μ F placed within 10 mm of the sensor is required.

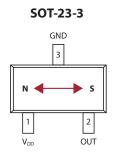
Application Circuit (SOT-23-3)



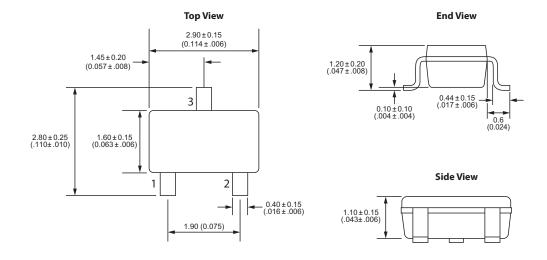




7 Axis of Sensitivity



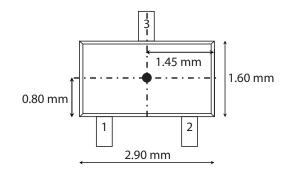
8 Dimensions SOT-23-3 Package Millimeters (Inches)



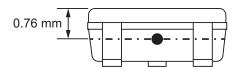


9 TMR Sensor Location

SOT-23-3 Package



SOT Package - Side View



10 Output Behavior vs. Magnetic Field Diagrams

Output Behavior vs. Magnetic Field - Bipolar

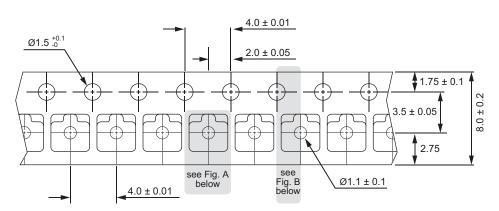
| Characteristics | Conditions | Output |
|-----------------|----------------------|------------|
| South Pole | B < B _{OPS} | Low (ON) |
| North Pole | B > B _{RPN} | High (OFF) |



12 TMR Sensor & Switch Packaging

SOT-23-3 Tape & Reel Packaging

Standard packaging is Tape & Reel containing 3,000 pieces. MSL Rating is 1.



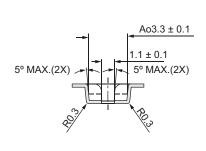
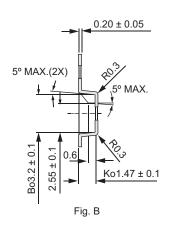
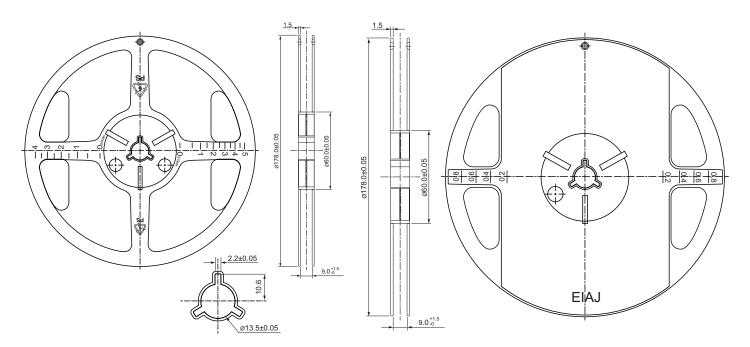


Fig. A



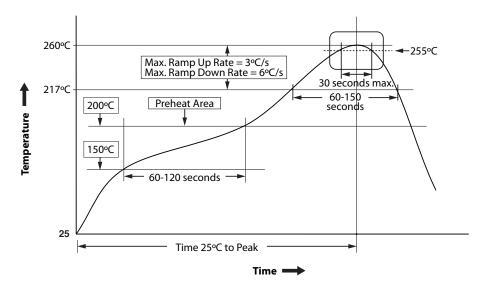




13 Suggested Pb-Free Reflow Profile

Notes:

- 1. Fully compatible with standard no-lead solder profile, 260 °C for 30 seconds max (3 cycles max).
- 2. Profile shown as example. Users are advised to develop their own board-level profile.
- 3. Suggested Pb-free reflow profile derived from IPC/JEDEC J-STD-020E.
- 4. Temperature tolerance: +0 °C, as measured at any point on the package or leads
- 5. MSL rating of 1 (SOT-23-3 only) compatible with J-STD-020 or equivalent.
- 6. MSL rating of 3 (LGA-4 and Leaded LGA-4) compatible with J-STD-020 or equivalent.
- 7. All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow (e.g., livebug). If parts are reflowed in other than the normal live bug assembly reflow orientation (i.e., dead-bug), Tp shall be within ± 2 °C of the live bug Tp and still meet the Tc requirements, otherwise, the profile shall be adjusted to achieve the latter. To accurately measure actual peak package body temperatures, refer to JEP140 for recommended thermocouple use.
- 8. Reflow profiles in this document are for classification/preconditioning and are not meant to specify board assembly profiles. Actual board assembly profiles should be developed based on specific process needs and board designs and should not exceed the parameters in this table.



14 Suggested Solder Pad Layout

SOT-23-3 Solder Pad Layout

