

QPS SERIES POSITIVE SWITCHING, DIGITAL ENCAPSULATED, TIME DELAY MODULES

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FEATURES

- C/MOS Digital Circuitry
- Time Delays To 1000 Minutes
- No First Cycle Effect
- Fully Solid State And Encapsulated
- 0.5% Repeat Accuracy
 Six Different Modes Of Operation
- Output Rated at 1 Ampere Continuous, 10 Amperes Inrush 0
- Fixed, Local Or Externally Adjustable Time Delays
- Small Size
- UL/cUL Pending

SPECIFICATIONS

1. Time Delay.

- 1.1 Type: C/MOS digital circuitry
- 1.2 Range: From 0.05 seconds to 1000 minutes. Fixed delays available (see time delay chart)
- 1.3 Repeat accuracy: ± 0.5% under fixed conditions
- 1.4 Setting accuracy: ± 10%
- 1.5 Reset time: 50 milliseconds maximum
- 1.6 Recycle time: 100 milliseconds during timing,
- 50 milliseconds after timing
- 1.7 Time delay vs. voltage and temperature: ± 2%

2. Input.

2.1 Operating voltage: 12, 24/28 & 36 VDC 2.2 Tolerance: ± 20% of nominal

3. Output.

- 3.1 Type: Solid state
- 3.2 Form: SPST
- 3.3 Rating: 1 amp steady state,(10 amp inrush,20 mA. min.)
- 3.4 Life: 100,000,000 operations minimum under full load

4. Protection.

- 4.1Transient: ± 1500 volts for 150 microseconds
- 4.2 Polarity: Reverse polarity protected
- 4.3 Dielectric breakdown: 1500 volts RMS minimum

5. Mechanical.

- 5.1 Mounting: One #8 or #10 screw
- 5.2 Termination: 1/4" quick connect terminals
- 5.3 Style: Surface mount encapsulated

6. Environmental.

- 6.1 Operating temperature: -20°C to +80°C
- 6.2 Storage temperature: -30°C to +85°C
- 6.3 Humidity: 95% relative non-condensing

MODE OF OPERATION - SERIES

DELAY ON MAKE - QMPS

Upon application of power to the input terminals, the time delay begins. At the completion of the pre-selected time delay, the output contact transfers. Reset is accomplished by removal of input power. There is no false output when reset during timing.



INTERVAL - QIPS

Upon application of power to the input terminals, the output contact immediately transfers and the time delay begins. At the completion of the pre-selected time delay, the output contact reverts to its original position. Reset is accomplished by removal of input power.



SINGLE SHOT - QSPS

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position. Removal of input power will reset the control.





DELAY ON BREAK - QBPS/QTPS *

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch, the output contact transfers and remains transferred if no further action is taken. When the initiate switch is opened, the time delay begins. At the completion of the pre-selected delay period the output contact reverts to its original position. Closure of initiate during timing will reset the delay period. Removal of input power will reset the control. *****QTPS is the same except it is trailing edge triggered. Load energizes when initiate switch is opened.

RETRIGGERABLE ONE-SHOT - QOPS

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position. **NOTE:** Momentary or maintained closure of initiate switch during timing will reset the time delay.



ORDERING INFORMATION			
SERIES	INPUT VOLTAGE	ADJUSTMENT	TIME DELAY RANGE
QBPS QIPS QMPS QOPS QSPS QTPS	1 - 12 VDC 2 - 24/28 VDC 9 - 36 VDC	0 - Local Adjust 1 - Fixed 2 - External Adjust	See Time Delay Range Chart